

ECE Building, University of Thessaly, Volos, Greece



September 22-26



1st summer school on

EDA Tool Validation and Verification in Chiplet Reliability

Exploring the Future of Semiconductors



1st business forum on

Shaping the Future of Reliable Electronics



September 25-26





MONDAY 22 SEPTEMBER

9:30 - 10:00	Welcome
10:00 - 10:45	Systems in the Stack: Hardware-aware Software for ML and Beyond - Dr. Nikela Papadopoulou, UGLA
10:45 - 11:15	Design of Energy Efficient and Dependable Architectures - Prof. Georgios Karakonstantis, UTH
11:15 - 11:45	Coffee Break
11:45 - 12:30	LLMs on the Edge: Applications, Challenges, and Opportunities - Dr. Tahani Aladwani, UGLA
12:30 - 13:00	EM/IR Mitigation Methodologies for Die-centric Power Grids - Dr. George Floros, UTH, TCD
13:00 - 14:00	Lunch
14:00 - 15:00	Semiconductor Reliability & the EDA Industry - Dr. George Floros, UTH, TCD
15:00 - 15:30	Coffee Break
15:30 - 16:15	AI-enabled Placement for 3D-DRAM Logic Layers - Dr. Thanasis Tziouvaras, Fotini Oikonomou, UTH
16:15 - 17:00	ML Workshop

TUESDAY 23 SEPTEMBER

9:30 - 10:00	Welcome
10:00 - 10:45	Physics-Based Electromigration Analysis in Complex VLSI Interconnects - Pavlos Stoikos, UTH
10:45 - 11:15	Scaling AI Efficiently: Pretraining and Fine-Tuning in the Era of 3D ICs - Dr. Yixian Shen, UvA
11:15 - 11:45	Coffee Break
11:45 - 12:30	Scaling AI Efficiently: Pretraining and Fine-Tuning in the Era of 3D ICs - Dr. Yixian Shen, UvA
12:30 - 13:00	HotPROTON: A System-Level Framework for Thermal-Aware Electromigration Analysis in Manycore Systems - Maria Pantazi, UTH
13:00 - 14:00	Lunch
14:00 - 15:00	Probabilistic SET Analysis and Mitigation Loop using Static Timing Analysis Principle - Christos Georgakidis, UTH
15:00 - 15:30	Coffee Break
15:30 - 16:15	TSV Modeling and Substrate Noise Coupling in 3D ICs: Impact on RF Oscillators - Ilias Exouzidis, UTH, NTUA
16:15 - 17:00	Networking & Discussions

WEDNESDAY 24 SEPTEMBER

9:30 - 10:00	Welcome
10:00 - 10:30	Introduction to EDA - George Goudroumanis, UTH
10:30 - 11:15	3DIC CAD Placement Flows and Algorithms - Dr. Nikolaos Sketopoulos, UTH, Co-founder Silicon Highway Technologies
11:15 - 11:45	Coffee Break
11:45 - 12:30	Designing for AI Efficiency: From Architectures to 3D IC - Dr. Lennart Bamberg, Chief Processor Architect, at NXP
12:30 - 13:00	IEEE Student Branch Activities
13:00 - 14:00	Lunch

14:00 - 15:00	3DPX: A 3D ASIC flow based on OpenSource Tools for Fast and Cost-effective Design Exploration - George Goudroumanis, UTH
15:00 - 15:30	Coffee Break
15:30 - 16:15	Designing PLLs in 3-D ICs - Prof. George Panagopoulos, NTUA
16:15 - 17:00	Introduction to Learning with Feedback: Theory and Applications - Dr. Shameem A Puthiya Parambath, UGLA

THURSDAY 25 SEPTEMBER

9:30 - 10:00	Registration
10:00 - 10:30	Welcome Session - TWINRelect, COIN-3D
10:30 - 10:40	Government Representative - Mr. Giannis Anastasios, Deputy Regional Governor of Thessaly for Digital Governance & Citizen Service
10:40 - 11:30	Ansys - Nikolas Perantakos, Application Engineer
11:30 - 12:00	Coffee Break
12:00 - 12:45	The CMOS 2.0 scaling paradigm - Moritz Brunion, Researcher Design-Technology Co-optimization, IMEC
12:45 - 13:15	Trimsignal - George Anagnostopoulos, Chief Software Architect
13:15 - 14:15	Lunch
14:15 - 15:00	Place & Route Flow: Challenges and Opportunities for Parallelism and AI - Dimitris Fotakis, Metis Electronic Design
15:00 - 15:30	ThessIC - Thrassos Axiotis, Director
15:30 - 16:00	Coffee Break
16:00 - 16:30	Space Network - Fotios Kotzakioulafis, Founder and Hub Manager, Dr. Georgios Kotlidas, Legal Lead and Founding
16:30 - 17:00	IHP - Dr. Krzysztof Herman, Research Assistant

FRIDAY 26 SEPTEMBER

9:30 - 10:00	Registration
10:00 - 10:30	HETiA - Dr. Sotirios Bantas, Board Member
10:30 - 11:00	Kenotom - Dr. Tilemahos Matiakis, Project Coordinator and Scientific Supervisor
11:00 - 11:30	Coffee Break
11:30 - 12:00	ArgoSemi - Dr. Konstantinos Vrysas, Co-Funder, Technical Director
12:00 - 12:30	Corallia - ESA BIC - Dr. Orfeas Voutyras, Programme Coordinator, Senior Associate
12:30 - 13:30	Lunch
13:30 - 14:30	Full Reference Video Quality Metrics - A Review - Dr. Ioannis Katsavounidis, Researcher Scientist - Meta, IEEE SPS
14:30 - 15:00	Smart Silicon - Nikos Katsaros, Digital Design and Concept Team Lead
15:00 - 15:30	Round Table
15:30 - 15:50	StarBound - University of Thessaly Student Team
15:50 - 17:00	Networking Session & Coffee Break
17:00 - 17:20	Closing Ceremony

COIN-3D

Exploring the future of semiconductors



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