Electra Palace Hotel Thessaloniki

May 4-8

3rd advanced course on

Integration of EDA Tools into Chiplet Design and Verification

COIN-3D

Exploring the future of semiconductors

Co-funded by the European Union

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COIN-3D – 3rd Advanced course: Integration of EDA Tools into Chiplet Design and Verification

Monday 4 May – Friday 8 May 2026, Thessaloniki, Greece

MONDAY 4 MAY

11:00 - 11:30	Welcome Coffee
11:30 - 11:45	Introduction to Low Power Design - Dr. Anuj Pathania, UvA
12:45 - 13:30	Lunch
13:30 - 14:15	Power-Efficient Inference on HMPSoCs - Dr. Anuj Pathania, UvA
14:15 - 15:00	Early-Stage Power Delivery Network Design and Optimization via Architectural Power Traces - Dr. George Floros
15:00 - 16:00	COIN-3D Project's Meeting

TUESDAY 5 MAY

11:00 - 11:30	Welcome Coffee
11:30 - 11:45	Optimization-aware Evaluation of Multi-Accelerator Systems for Monolithic 3D - Prof. Alberto Garcia-Ortiz, UBREMEN
12:45 - 13:30	Lunch
13:30 - 14:15	PDN for 3D technologies - Prof. Alberto Garcia-Ortiz, UBREMEN
14:15 - 15:00	RISC-V: A Test Case Suited for 3D - Nikos Kostakis, UTH
15:00 - 16:00	Problem Solving - Knowledge Transfer

WEDNESDAY 6 MAY

11:00 - 11:30	Welcome Coffee
11:30 - 11:45	Towards a physically valid model of placement optimization - Dr. Philip Christie, TCD
12:45 - 13:30	Lunch
13:30 - 14:15	Analytical TSV Modeling and Ring VCO Substrate Noise Coupling for 3D Chiplet Reliability - Elias Exouzidis, UTH, NTUA
14:15 - 15:00	Exploring Graph-based Reinforcement Learning Architectures for Detailed Placement - Foteini Oikonomou, UTH
15:00 - 16:00	Problem Solving - Knowledge Transfer

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THURSDAY 7 MAY

11:00 - 11:30	Welcome Coffee
11:30 - 11:45	ASIC Physical Design Future Challenges - George Goudroumanis, UTH
12:45 - 13:30	Lunch
13:30 - 14:15	From 2D to 3D: Unlocking Heterogeneous Placement in OpenROAD - Maria Pantazi, UTH
14:15 - 15:00	Temperature-Aware Compact SER Modeling Using Model Order Reduction - Dr. Pavlos Stoikos, UTH
15:00 - 16:00	Problem Solving - Knowledge Transfer

FRIDAY 8 MAY

11:00 - 11:30	Welcome Coffee
11:30 - 11:45	Hardware-Guided Search for Deployable Deep Learning Models - Dr. Tahani Aladwani, UGLA
12:45 - 13:30	Lunch
13:30 - 14:15	Constraint-Guided Personalisation of Edge Deep Learning Models - Dr. Tahani Aladwani, UGLA
14:15 - 15:00	Closing Ceremony

Electra Palace Hotel Thessaloniki

Old Gate



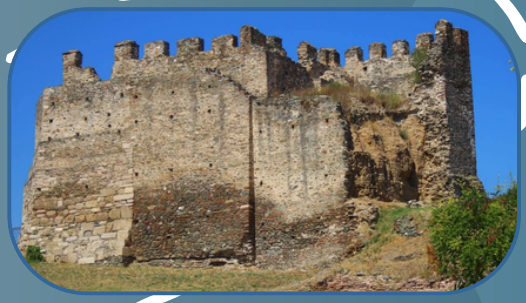


Heptapyrgion of Thessaloniki



Tower of Manuel Palaiologos

Acropolis Cistern





Castles of Thessaloniki



Acropolis Walls



Anna Palaiologina Gates

Trigonion Tower



Andronikos Lapardas Tower



Toixo - Toixo Tavern